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### AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listing of claims in the Application. Please amend the claims to read as follows and cancel without prejudice or disclaimer the claims marked as canceled:

#### Listing of Claims

1. (Currently Amended) A method comprising:  
receiving through an antenna of a radio frequency module a pilot signal performing processing operations at a first clock rate during at least part of a first time period of a wake period of a slotted mode; in which a radio frequency module receives through an antenna incoming wireless communication signals carrying data and stores said data; and  
deactivating said radio frequency module;  
determining a pseudo-random noise offset performing background processing of at least a portion of said received signals pilot signal at a second, faster clock rate during at least part of a second time period of said wake period; and in which said radio frequency module is de-activated.  
synchronizing said radio frequency module to said determined offset if said module is out of step with said determined offset.
2. (Currently Amended) The method according to claim 1 wherein performing background processing comprises processing said pilot signal is a spread spectrum [[signals]] signal.
3. (Currently Amended) The method according to claim 1 wherein performing background processing comprises processing said pilot signal is a Code Division Multiple Access (CDMA) [[information]] signal.

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4. **(Currently Amended)** The method according to claim 1, further comprising at least one of ~~wherein performing background processing comprises performing at least one of synchronizing pseudorandom noise (PN) offset of said received signals;~~ searching for at least one neighboring communications cell and searching for at least one candidate communications cell.
5. **(Canceled).**
6. **(Previously Presented)** The method according to claim 1 comprising receiving a carrier during at least part of said second time period.
7. **(Canceled)**
8. **(Currently Amended)** The method according to claim [[7]] 1 comprising reducing the power consumed during said at least one wake period after recording said received signals.
9. **(Canceled)**
10. **(Currently Amended)** A receiver comprising:  
an antenna;  
a radio frequency module coupled to said antenna for receiving a pilot signal at a first clock rate during at least part of a first time period of a wake period of a slotted mode; and adapted to receive through an antenna wireless communication signals and to store at least a portion of the received signals during a first time period and to be de-activated during a second time period; and  
a processor for deactivating said radio frequency module after said pilot signal is received and for determining a pseudo-random noise offset of said pilot signal at a second, faster clock rate during at least part of a second time period of said wake period and for synchronizing said radio frequency module to said determined offset

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~~if said module is out of step with said determined offset, adapted to operate at a first clock rate during at least part of said first time period, to de-activate the radio frequency module for said second time period, and to perform background processing of at least said portion of received signals at a second, faster clock rate during at least part of said second time period.~~

11. (Currently Amended) The receiver according to claim 10 wherein said radio frequency module comprises a memory device ~~[[adapted]]~~ for storing ~~[[therein]]~~ at least a ~~[[said]]~~ portion of said pilot signal ~~received signals~~, and wherein said processor ~~[[comprising]]~~ comprises a digital processing unit, wherein said memory device is adapted to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said digital processing unit.
12. (Currently Amended) The receiver according to claim 10 wherein said radio frequency module comprises a memory device ~~[[adapted]]~~ for storing ~~[[therein]]~~ at least a ~~[[said]]~~ portion of said pilot signal ~~received signals~~, and wherein said processor comprises a rake receiver and a search engine, wherein said memory device is adapted to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said rake receiver and said search engine.
13. (Currently Amended) The receiver according to claim 11 comprising a sampling unit adapted to receive said portion of said pilot signal ~~received signals~~ and to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said memory device.
14. (Currently Amended) The receiver according to claim 12 comprising a sampling unit adapted to receive said portion of said pilot signal ~~received signals~~ and to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said memory device.
15. (Currently Amended) A cellular communication system comprising:  
an antenna;

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a radio frequency module coupled to said antenna for receiving a pilot signal at a first clock rate during at least part of a first time period of a wake period of a slotted mode; and adapted to receive through an antenna wireless communication signals ~~and to store at least a portion of the received signals during a first time period and to be de-activated during a second time period; and~~  
a processor for deactivating said radio frequency module after said pilot signal is received and for determining a pseudo-random noise offset of said pilot signal at a second, faster clock rate during at least part of a second time period of said wake period and for synchronizing said radio frequency module to said determined offset if said module is out of step with said determined offset, adapted to operate at a first clock rate during at least part of said first time period, to de-activate the radio frequency module for said second time period, and to perform background processing of at least said portion of received signals at a second, faster clock rate during at least part of said second time period.

16. (Currently Amended) The cellular communication system according to claim 15 wherein said radio frequency module comprises a memory device ~~[[adapted]]~~ for storing ~~[[therein]]~~ at least a [[said]] portion of said pilot signal ~~received signals~~, and said processor comprises a digital processing unit, wherein said memory device is adapted to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said digital processing unit.
17. (Currently Amended) The cellular communication system according to claim 15 wherein said radio frequency module comprises a memory device ~~[[adapted]]~~ for storing ~~[[therein]]~~ at least a [[said]] portion of said pilot signal ~~received signals~~, and said processor comprises a rake receiver and a search engine, wherein said memory device is adapted to ~~[[input]]~~ output said portion of said pilot signal ~~received signals~~ to said rake receiver and said search engine.
18. (Currently Amended) The cellular communication system according to claim 16

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comprising a sampling unit adapted to receive said portion of said pilot signal received signals and to ~~[[input]]~~ output said portion of said pilot signal received signals to said memory device.

19. (Currently Amended) The cellular communication system according to claim 17 comprising a sampling unit adapted to receive said portion of said pilot signal received signals and to ~~[[input]]~~ output said portion of said pilot signal received signals to said memory device.